

# ABSTRACT OF THE DISCLOSURE

When a transistor is turned ON, a predetermined voltage  $V_{CL}$  is applied to a minus terminal of a voltage comparator through a C terminal. A predetermined voltage  $V_d$  ( $V_d > V_{CL}$ ) is applied to a plus terminal of the voltage comparator, and an output of the voltage comparator changes from a low level to a high level. Such change is detected by a trigger detection circuit so as to activate a timer circuit, and an output of the timer circuit transitions to the high level during a predetermined time. Thereby, a transistor is changed to an on-state, a reference voltage  $V_b$  of an adjustment voltage applied to a plus terminal of a voltage comparator is set generally to 0V, and a transistor is turned OFF, so as to temporarily suspend a power generation state of a vehicular generator.

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